


JC916 U.S. PTO 09/676548  09/23/00			
	Class	Subclass	ISSUE CLASSIFICATION

~~PATENT~~ NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>KW AC</u> Q.A. <u>A-G</u>	PATENT DATE
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APPLICATION NO. 09/676548	CONT/PRIOR D	CLASS <del>429</del> 361	SUBCLASS <del>150</del>	ART UNIT <del>1772</del>	EXAMINER <u>LOWEY</u>
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## APPLICANTS

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DATE Jackson  
NF LAO TERMINAL D  
OCT 9-6-03

# TITLE

Surface structure and method of making, and electrostatic wafer clamp, incorporating surface structure

PTO-2040  
12/89

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<b>ISSUING CLASSIFICATION</b>								
<b>ORIGINAL</b>			<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>	<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____    	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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